

## Publikationen

(2017): Empirical results on parity-based soft error detection with software-based retry. In: Microprocessors and Microsystems, vol. 48, no. February, pp. 62-68. DOI: 10.1016/j.micpro.2016.09.009.

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(2015): Parity-based Soft Error Detection with Software-based Retry vs. Triplication-based Soft Error Correction - An Analytical Comparison on a Flash-based FPGA Architecture. In: Informatik 2015 - Informatik, Energie und Umwelt (28.09.-02.10.2015; Cottbus), Bonn, vol. P-246.

(2015): In-circuit Error Detection with Software-based Error Correction - An Alternative to TMR. In: Formal Modeling and Verification of Cyber-Physical Systems.

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